

Notice of References Cited	Application/Control No. 09/311,313	Applicant(s)/Patent Under Reexamination MCBRIDE, JOHN G	
	Examiner Leigh Marie Garbowski	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	J. W. Gannett, "Extending an FET Layout Verification System to Bipolar Technology," IEEE 1988 Bipolar Circuit & Technology Meeting, pages 183-186.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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